

Chip Termination 150 Watts, 50Ω



The A150N50X4B is high performance Aluminum Nitride (AIN) chip termination intended as a cost competitive alternative to Beryllium Oxide (BeO). The termination is well suited to all cellular frequency bands such as; AMPS, GSM, DCS, PCS, PHS and UMTS. The high power handling makes the part ideal for terminating circulators, and for use in power combiners. The termination is also RoHS compliant!

General Specifications

Resistive Element Thick film
Substrate AIN Ceramic

Terminal Finish Matte Tin over Nickel Barrier

Operating Temperature -55 to +200°C (see de rating chart)

Tolerance is ± 0.010 ", unless otherwise specified. Designed to meet or exceed applicable portions of MIL-E-5400. All dimensions in inches.

Electrical Specifications

Resistance Value:50 Ohms, \pm 2%Power:150 WattsFrequency Range:DC - 2.7 GHzReturn Loss>26dB to 2.0 GHz>20dB to 2.7 GHz

Specification based on unit properly installed using suggested mounting instructions and a 50 ohm nominal impedance. **Specifications subject to change.**

Element

Features:

RoHS Compliant

Low VSWR

150 Watts

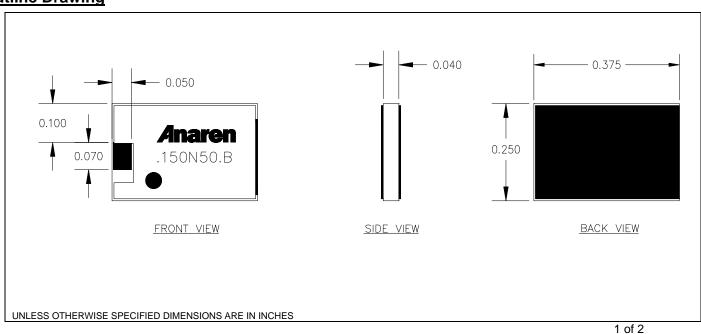
DC - 2.7 GHz

AIN Ceramic

Non-Nichrome Resistive

100% Tested

Outline Drawing



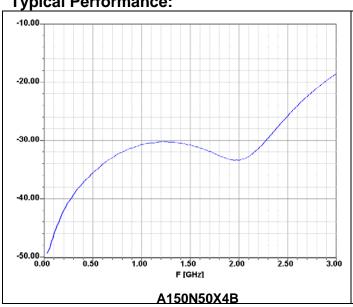


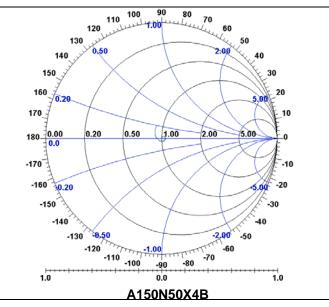


Available on Tape and Reel For Pick and Place Manufacturing. USA/Canada: (315) 432-8909 Toll Free: (800) 544-2414 Europe: +44 2392-232392



Typical Performance:

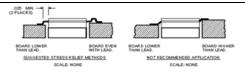




Power De-rating:

POWER DERATING POWER 100 75. RATED 50 25 -R 0 -25 75 50 100 125 150 175 200 SOLDER INTERFACE TEMPERATURE - C *Actual performance could be limited by the solder properties of the application

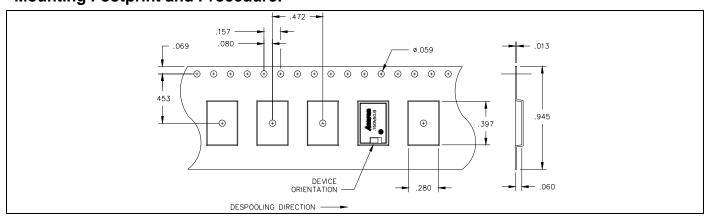
Mounting Footprint and Procedure:



SUGGESTED MOUNTING PROCEDURE

- MAKE SURE THAT THE DEVICES ARE MOUNTED ON FLAT SURFACES (.001" UNDER THE DEVICE) TO OPTIMIZE THE HEAT TRANSFER.
- POSITION DEVICE ON MOUNTING SURFACE AND SOLDER IN PLACE USING AN APPROPRIATE SOLDER.
- SOLDER LEADS IN PLACE USING AN APPROPRIATE SOLDER TYPE WITH A CONTROLLED TEMPERATURE IRON.

Mounting Footprint and Procedure:



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