

1. General description

Planar passivated Silicon Controlled Rectifier (SCR) in a SOT186A (TO-220F) plastic package intended for use in applications requiring high bidirectional blocking voltage capability, high current inrush capability and high thermal cycling performance.

2. Features and benefits

- AC power control
- High bidirectional blocking voltage capability
- High thermal cycling performance
- Planar passivated for voltage ruggedness and reliability
- Package meets UL1557 isolation test requirement rated at 2500V RMS
- Package meets UL94V0 flammability requirement
- Package is RoHS compliant
- Very high immunity to false turn-on by dv/dt and IEC 61000-4-4 fast transient

3. Applications

- Capacitive Discharge Ignition (CDI)
- Crowbar protection
- Inrush protection
- Motor control
- Voltage regulation

4. Quick reference data

Table 1. Qui	ck reference data					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{DRM}	repetitive peak off- state voltage		-	-	800	V
V _{RRM}	repetitive peak reverse voltage		-	-	800	V
I _{T(AV)}	average on-state current	half sine wave	-	-	16	A
I _{T(RMS)}	RMS on-state current	half sine wave; T _h ≤ 30 °C; <u>Fig. 1;</u> <u>Fig. 2; Fig. 3</u>	-	-	25	A
I _{TSM}	non-repetitive peak on- state current	half sine wave; T _{j(init)} = 25 °C; t _p = 10 ms; <u>Fig. 4; Fig. 5</u>	-	-	300	A
Static chara	acteristics					
I _{GT}	gate trigger current	V _D = 12 V; I _T = 0.1 A; T _j = 25 °C; <u>Fig. 7</u>	-	-	35	mA

5. Pinning information

Pin 1	Symbol K	Description cathode	Simplified outline	Graphic symbol
1	К	cathode		
0		calload	mb	А- Д -К
2	А	anode		Ġ sym037
3	G	gate		Symoor
mb	n.c.	mounting base; isolated	() () () () () () () () () () () () () (

6. Ordering information

Table 3. Ordering information							
Type number	Package						
	Name	Description	Version				
BT145X-800R	TO-220F	plastic single-ended package; isolated heatsink mounted; 1 mounting hole; 3-lead TO-220 "full pack"	SOT186A				

7. Marking

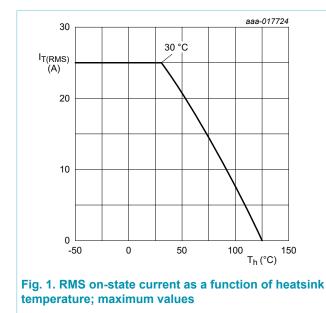
Table 4. Marking codes				
Type number	Marking code			
BT145X-800R	BT145X-800R			

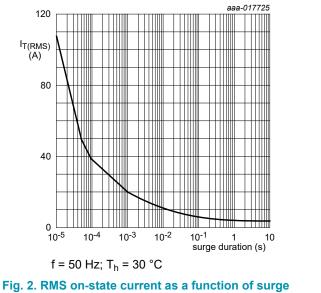
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{DRM}	repetitive peak off-state voltage		-	800	V
V _{RRM}	repetitive peak reverse voltage		-	800	V
I _{T(AV)}	average on-state current	half sine wave	-	16	А
I _{T(RMS)}	RMS on-state current	half sine wave; $T_h \le 30$ °C; <u>Fig. 1</u> ; <u>Fig. 2</u> ; <u>Fig. 3</u>	-	25	A
I _{TSM}	non-repetitive peak on- state current	half sine wave; $T_{j(init)}$ = 25 °C; t_p = 10 ms; Fig. 4; Fig. 5	-	300	A
		half sine wave; T _{j(init)} = 25 °C; t _p = 8.3 ms	-	330	А
l ² t	I ² t for fusing	t _p = 10 ms; SIN	-	450	A²s
dl _T /dt	rate of rise of on-state current	I _G = 70 mA	-	200	A/µs
I _{GM}	peak gate current		-	5	А
V _{RGM}	peak reverse gate voltage		-	5	V
P _{GM}	peak gate power		-	20	W
P _{G(AV)}	average gate power	over any 20 ms period	-	0.5	W
T _{stg}	storage temperature		-40	150	°C
Tj	junction temperature		-	125	°C



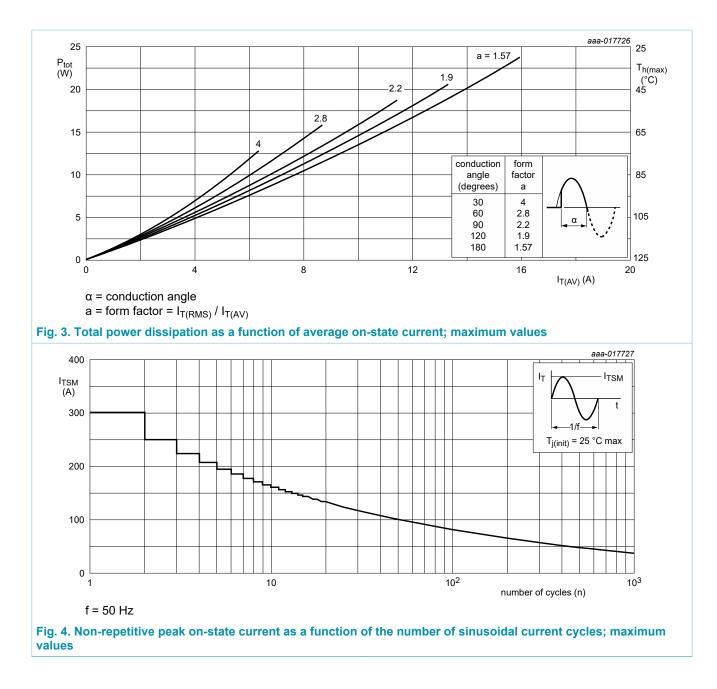


duration; maximum values

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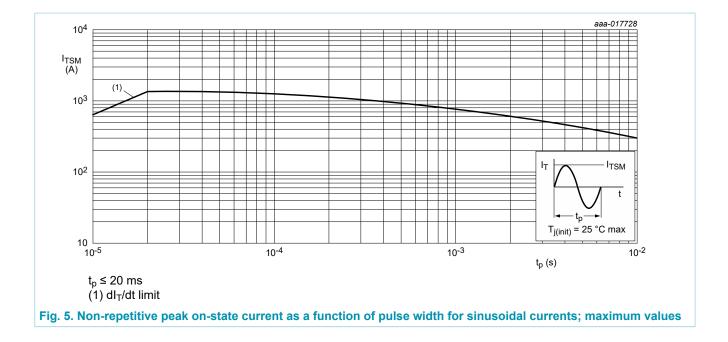
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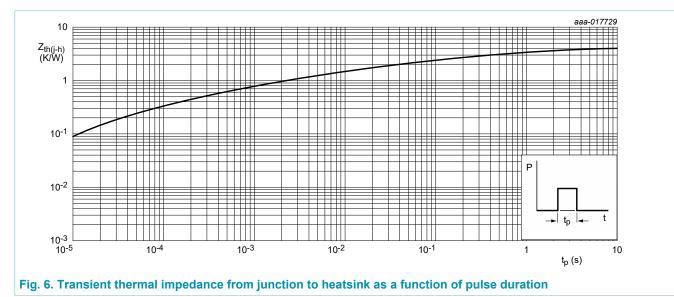
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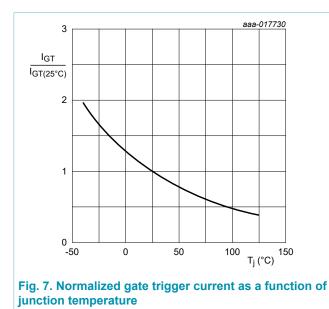
9. Thermal characteristics

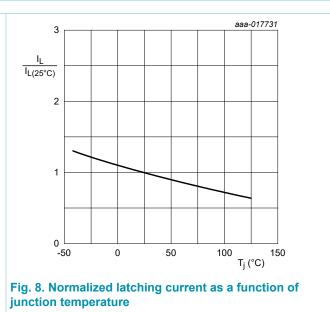
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-h)}	thermal resistance from junction to heatsink	full cycle or half cycle; with heatsink compound; Fig. 6	-	-	4	K/W
		full cycle or half cycle; without heatsink compound	-	-	5.5	K/W
R _{th(j-a)}	thermal resistance from junction to ambient free air	in free air	-	55	-	K/W



10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics					
I _{GT}	gate trigger current	V_D = 12 V; I _T = 0.1 A; T _j = 25 °C; <u>Fig. 7</u>	-	-	35	mA
IL .	latching current	V_D = 12 V; I _G = 0.1 A; T _j = 25 °C; <u>Fig. 8</u>	-	-	80	mA
I _H	holding current	V _D = 12 V; T _j = 25 °C; <u>Fig. 9</u>	-	-	60	mA
V _T	on-state voltage	I _T = 30 A; T _j = 25 °C; <u>Fig. 10</u>	-	1.1	1.5	V
V _{GT}	gate trigger voltage	V _D = 12 V; I _T = 0.1 A; T _j = 25 °C; <u>Fig. 11</u>	-	0.6	1	V
		V _D = 800 V; I _T = 0.1 A; T _j = 125 °C; Fig. 11	0.25	0.4	-	V
I _D	off-state current	V _D = 800 V; T _j = 125 °C	-	0.2	1	mA
I _R	reverse current	V _R = 800 V; T _j = 125 °C	-	0.2	1	mA
Dynamic ch	naracteristics				·	
dV _D /dt	rate of rise of off-state voltage	V_{DM} = 536 V; T _j = 125 °C; exponential waveform; gate open circuit	200	-	-	V/µs
t _{gt}	gate-controlled turn-on time	I_{TM} = 40 A; V _D = 800 V; I _G = 0.1 mA; dI _G /dt = 5 A/µs; T _j = 25 °C	-	2	-	μs
t _q	commutated turn-off time	V_{DM} = 536 V; T _j = 125 °C; I _{TM} = 50 A; V_R = 25 V; (dI _T /dt) _M = 30 A/µs; dV _D / dt = 50 V/µs	-	70	-	μs

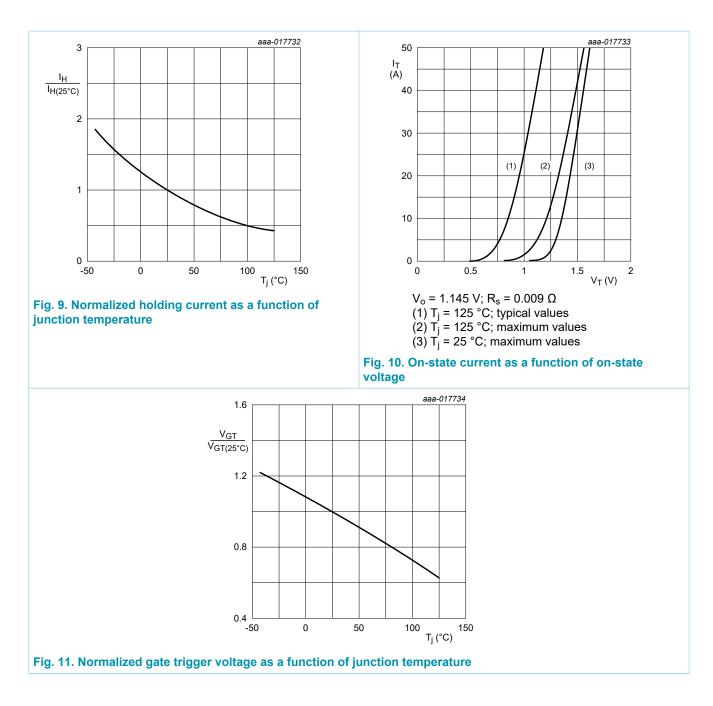




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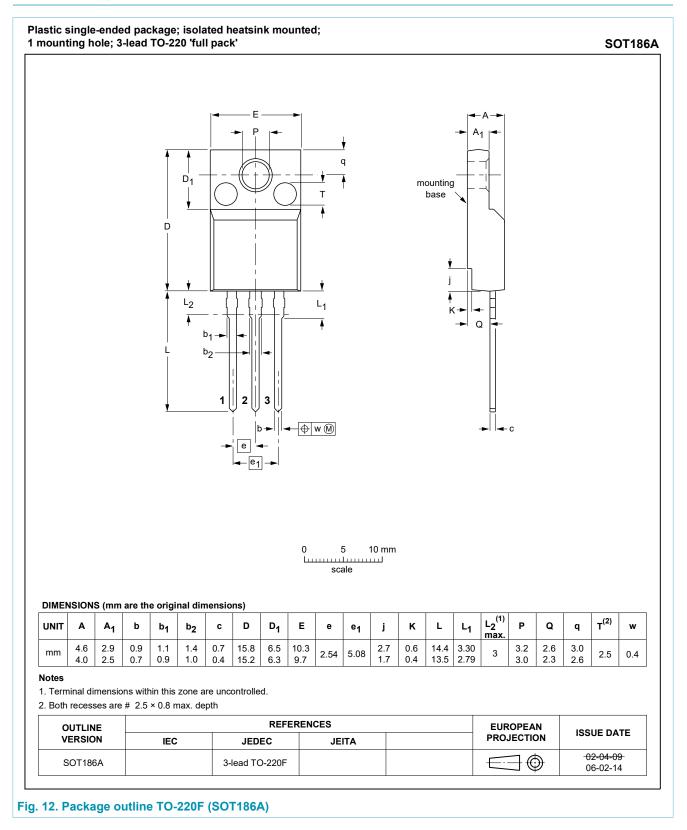
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11. Package outline



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12. Legal information

Data sheet status

Document status [1][2]	Product status [<u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <u>http://www.ween-semi.com</u>.

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BT145X-800R

13. Contents

1.	General description	.1
2.	Features and benefits	1
3.	Applications	1
4.	Quick reference data	.1
5.	Pinning information	.2
6.	Ordering information	.2
7.	Marking	.2
8.	Limiting values	3
9.	Thermal characteristics	6
10.	Characteristics	.7
11.	Package outline	9
	Legal information 1	

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